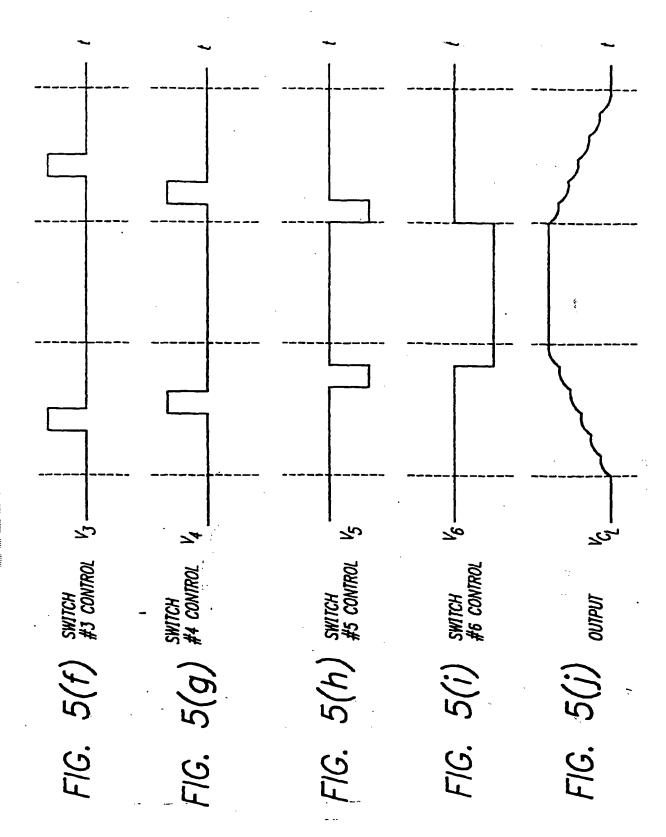
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FIG. 1 PRIOR ART

FIG. 2 PRIOR ART

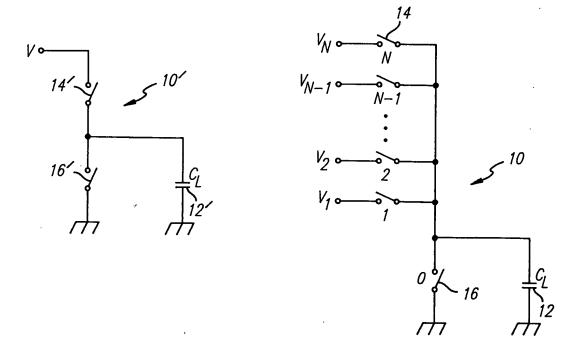
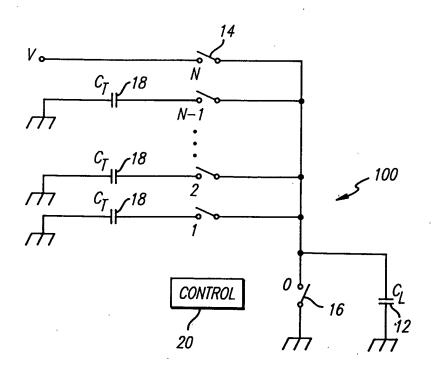


FIG. 3



90AD十 VouT DRAF (SFLY) 2] OUTPUT LOGIC INPUT LOGIC LATCH  $\sim$ INPUT 5 FINITE STATE MACHINE 70 70 74 70 - X2072 202

F1G.

DRAFISTAN

